

**AMENDMENTS TO THE SPECIFICATION**

Please amend the paragraph at page 4, lines 8-17 as follows:

The signal value on a photosensitive pixel element 104 is switched through the source-follower transistor, the row select transistor 102 (controlled by a control signal ROW), and the transistor 110 to the capacitor  $C_1$ . After the capacitor  $C_1$  is charged to the proper voltage, the photosensitive element 104 is reset using the reset switch 108. The reset level of the pixel 101 is sampled by the transistor 112, and stored on the capacitor  $C_2$ . The negative plates of the capacitors  $C_1$ ,  $C_2$  can be clamped during appropriate times. The capacitors are clamped at a reference voltage ( $V_{ref}$ ) by closing associated switches 122, 124, respectively. Similarly, the positive plates of the capacitors  $C_1$ ,  $C_2$  can be clamped at the same time as the negative plates by causing transistor 118 to conduct via the control signal CB.